# **SP10T 18GHz**

## Normally open

#### ◆ RF Features

RF Range (GHz)	Insertion loss (dB)	Isolation(dB)	Standing wave
DC -6	0.3	70	1.3
6-12	0.4	60	1.4
12 - 18	0.5	50	1.5

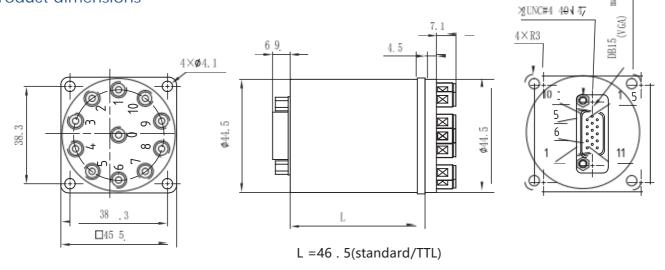


### Operating voltage/current

Operating voltage (V)		12	24	28
Current(mA)	Normally Open	300	150	140

<sup>\*</sup> The voltage can be selected according to user's requirements.

### Product dimensions



### ◆ Technical specifications

Switching sequence: first break and then

close

Switching rate: <15ms
Operating temperature:

-25°C~65°C (standard)

-55°C~85°C(Temperature expansion)

Switching life: 2 million times

RF connector: SMA Female

Control interface: DB15 Male

Impact (non-working state): 30G, 1/2 Sine, 11ms

Vibration (operating state): 20-2000Hz,

10G RM

# **SP10T 26.5GHz**

## Normally open

#### RF Features

RF Range (GF	z) Insertion loss (dB)	Isolation (dB)	Standing wave
DC -6	0.3	70	1.3
6-12	0.4	60	1.4
12 - 18	0.6	50	1.6
18 -26. 5	0.7	50	1.7

### Operating voltage/current

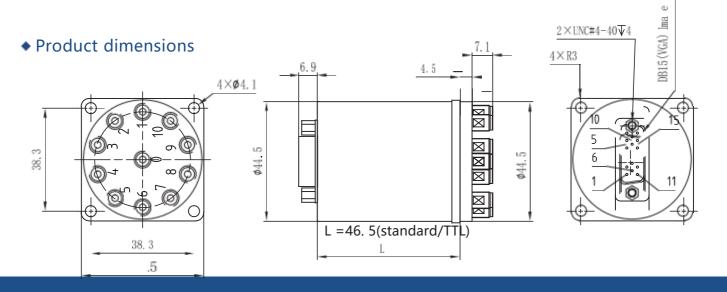
Operating voltage (V)		12	24	28
Current(mA)	Normally Open	300	150	140

\* The voltage can be selected according to user's requirements.



### **Product features**

- DC to 26.5GHz
  - Low SWR, low loss, high isolation
- Connector form SMA
- TTL level control is selectable



### **Technical specifications**

Switching sequence: first break and then close

Switching rate: <15ms

Operating temperature: -25°C~65°C (standard)

55°C~85°C(Temperature expansion)

Switching life: 2 million times

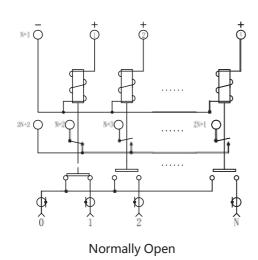
RF connector: SMA Female

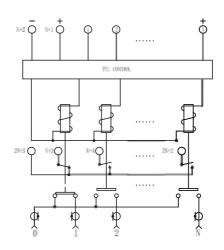
Control interface: DB15 Male

Impact (non-working state): 30G, 1/2 Sine, 11ms

Vibration (operating state): 20-2000Hz,

**10G RM** 





Normally Open+TTL

Switching method SPnT, n=9、10		RF Channel	Pin definition		
			DB15/DB25 MALE		
			Motivation	Feedback	
Normally open	NO TTL	0→1	1:VDC, n+1:GND	2n+2→ n+2	
		0→2	2:VDC,n+1:GND	2n+2→ n+3	
		0→ n	n:VDC,n+1:GND	2n+2→2n+1	
	TTL	0→1	1:TTL , n+1:VDC , n+2:GND	2n+3→ n+3	
		0→2	2:TTL , n+1:VDC , n+2:GND	2n+3→ n+4	
		0→ n	n:TTL , n+1:VDC , n+2:GND	2n+3→2n+2	